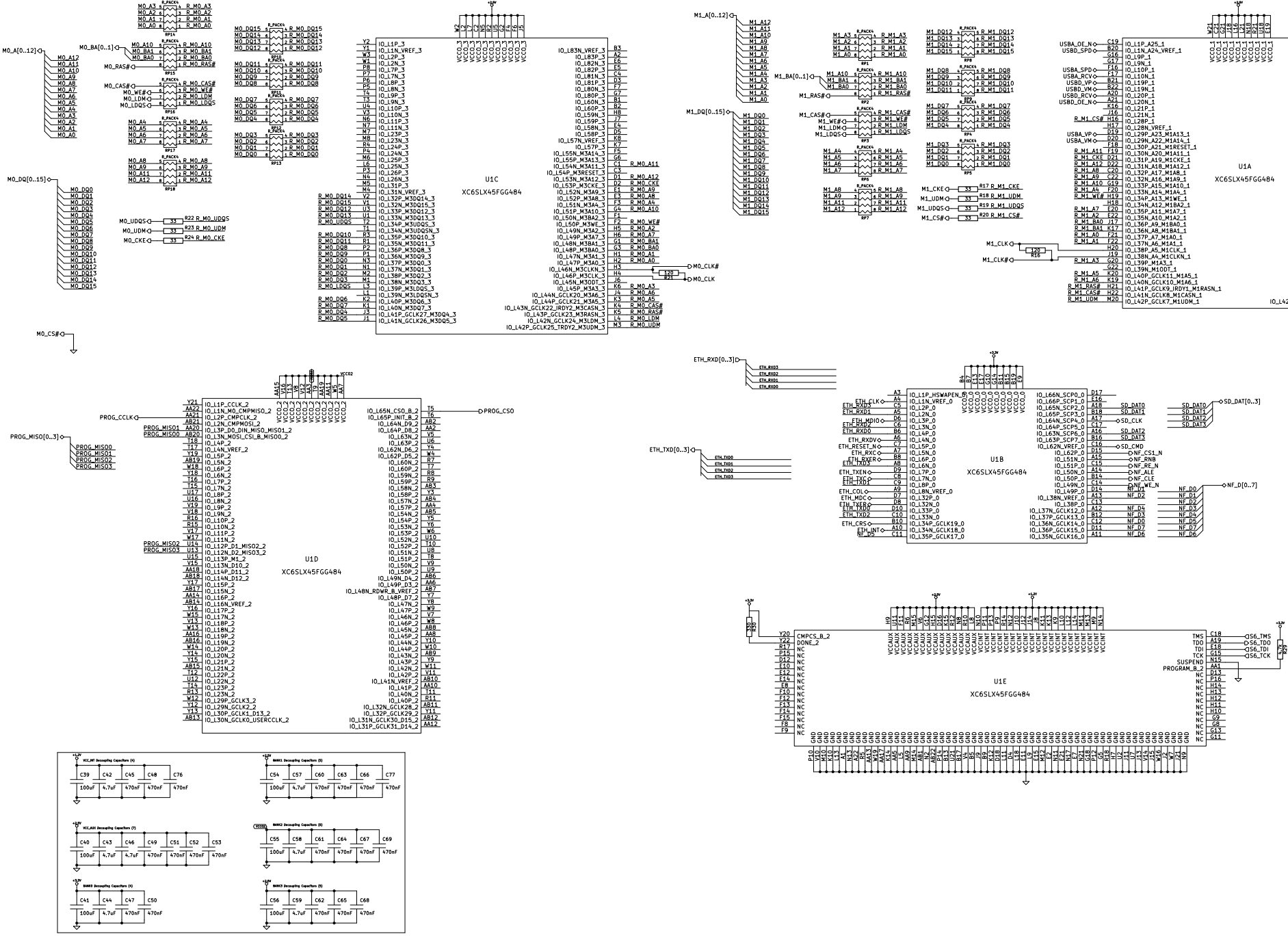
U1C
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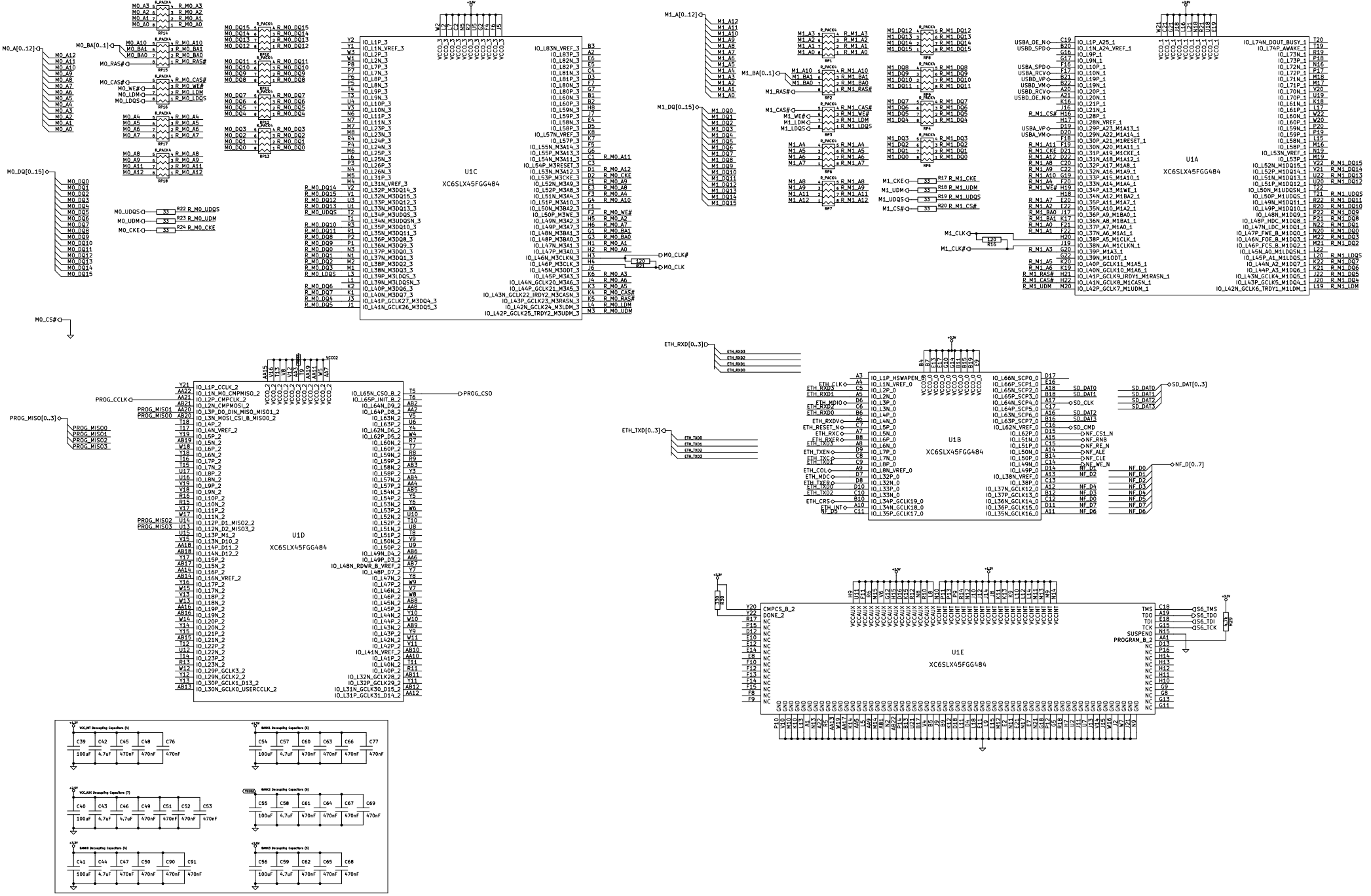
U1A
XC6SLX45FGG484
U1B
XC6SLX45FGG484
U1D
XC6SLX45FGG484
U1E
XC6SLX45FGG484
M0_A[0..12]
M0_BA[0..1]
M0_RAS#
M0_CAS#
M0_WE#
M0_LDM
M0_LDQS
M0_DQ[0..15]
M0_UDQS
M0_UDM
M0_CKE
M0_CS#
M0_CLK#
M0_CLK
M1_A[0..12]
M1_BA[0..1]
M1_RAS#
M1_CAS#
M1_WE#
M1_LDM
M1_LDQS
M1_DQ[0..15]
M1_CKE
M1_UDM
M1_UDQS
M1_CS#
M1_CLK
M1_CLK#
PROG_CCLK
PROG_MISO[0..3]
PROG_CSO
ETH_RXD[0..3]
ETH_TXD[0..3]
SD_DAT[0..3]
NF_D[0..7]
VCC_INT Decoupling Capacitors (4)
BANK1 Decoupling Capacitors (5)
VCC_AUX Decoupling Capacitors (7)
BANK2 Decoupling Capacitors (6)
BANK3 Decoupling Capacitors (4)
BANK3 Decoupling Capacitors (5)


U1C
XC6SLX45FGG484
U1A
XC6SLX45FGG484
U1B
XC6SLX45FGG484
U1D
XC6SLX45FGG484
U1E
XC6SLX45FGG484
M0_A[0..12]
M0_BA[0..1]
M0_RAS#
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M0_LDM
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M0_UDM
M0_CKE
M0_CS#
M0_CLK#
M0_CLK
M1_A[0..12]
M1_BA[0..1]
M1_RAS#
M1_CAS#
M1_WE#
M1_LDM
M1_LDQS
M1_DQ[0..15]
M1_CKE
M1_UDM
M1_UDQS
M1_CS#
M1_CLK
M1_CLK#
PROG_CCLK
PROG_MISO[0..3]
PROG_CSO
ETH_RXD[0..3]
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SD_DAT[0..3]
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VCC_INT Decoupling Capacitors (4)
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BANK3 Decoupling Capacitors (5)